Express Mail, Airbill No. EV 262795624

#### **Application Data Sheet**

## **Application Information**

Application Type:: Regular

Subject Matter:: Utility

Suggested Group Art Unit:: N/A

CD-ROM or CD-R?:: None

Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: NOISE-REDUCING TRANSISTOR

ARRANGEMENT, INTEGRATED CIRCUIT,

AND METHOD FOR REDUCING THE

NOISE OF FIELD EFFECT TRANSISTORS

Attorney Docket Number:: V0195.0080

Request for Early Publication?:: No

Request for Non-Publication?:: No

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

#### **Applicant Information**

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Germany

Status:: Full Capacity

Given Name:: Ralf

Family Name:: Brederlow

City of Residence:: Munich

Country of Residence:: Germany

Applicant Authority Type::

Primary Citizenship Country:: Republic of Korea

Inventor

Status:: Full Capacity

Given Name:: Jeongwook

Family Name:: Koh

Country of Residence:: Germany

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Germany

Status:: Full Capacity

Given Name:: Christian

Family Name:: Pacha

Country of Residence:: Germany

Applicant Authority Type:: Inventor

Primary Citizenship Country:: Germany

Status:: Full Capacity

Given Name:: Roland

Family Name:: Thewes

Country of Residence:: Germany

**Correspondence Information** 

Correspondence Customer Number:: 38881

Representative Information

Representative Customer Number:: 38881

**Domestic Priority Information** 

# **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Germany	103 58 713.6	12/15/03	Yes

### **Assignee Information**

Assignee name::

INFINEON TECHNOLOGIES AG

Street of mailing address::

St.-Martin-Strasse 53

City of mailing address::

Munich

Country of mailing address::

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Postal or Zip Code of mailing address::

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